

Vulnerability to Flash Controller for Secure USB Drives

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Abstract

This paper analyzes a vulnerability in the flash controller for secure USB drives, which is meant to enable secure USB drives to prevent unauthorized access to the data stored on them. This controller divides a driver into several partitions, one of which is configured as a secure area to store secret information. Generally, secure USB drives supporting multiple partitions may have three different areas, such as a CD-ROM area, a secure area, and a hidden area. The CD-ROM area stores the application software that manages security functions, the secure area stores the data that users wish to protect, and the hidden area stores secure information for user authentication. In this environment, it is a requirement that no one can access the data stored in the secure area when the user authentication fails. Nevertheless, attackers can access the secure area if they manipulate a vulnerability in the flash controller within the USB flash drive. In this paper, we analyze and verify this vulnerability. We expect our results will provide manufacturers with useful information for making a more secure USB flash controller.

Keywords: Vulnerability Analysis, Secure USB drive, Flash Controller

1 Introduction

USB flash drives have become prevalent in the last decade due to their portability and availability. However, the USB specification on which USB flash drives are based does not support data protection by itself, since the protocol did not consider security applications at the outset [6]. Thus, the data stored in a USB flash drive should be protected by hardware manufacturers or software developers through additional security technologies. This type of protected drive is commonly called a Secure USB Flash Drive and it has been recommended to use these secure USB drives to protect secret data stored on them.

Functions for protecting data in a USB flash drive consist of user authentication and data encryption/decryption. The user authentication function is responsible for access control to the stored data. The encryption/decryption function is responsible for transforming the secret data. In realizing these functions, various implementations have been found, though they are categorized into three different approaches; one is software-based, another one is hardware-based partitioning, and another one is hardware-based. The software-based approach performs both functions for user authentication and encryption/decryption within the application software, outside the drive. The hardware-based partitioning method supports multiple partitions in one physical drive, divided by a hardware device. The hardware-based method encrypts or decrypts the stored data inside the USB flash drive by generating the key within it.

Even though they incorporate these security functions explained above, some secure USB memory devices have various vulnerabilities due to design and implementation mistakes. These vulnerabilities can leak the stored data [8, 5, 7, 1, 2]. Most such vulnerabilities can be detected through reverse

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engineering that is one of the analyzing methods of security functions, and attackers can bypass user authentication and steal decryption keys by using the results of that analysis. Published research reports on hardware modules have not included detailed explanations of these kinds of attacks and there has not been much recent research on this in any case, so new studies are needed. Therefore, in this paper, we select the SM3254 chipset that is supported by Silicon Motion Corporation [9], as a commonly used secure USB flash drive controller that utilizes dividing partitions in USB memory, and analyze the operating process of it. We illustrate the results of our vulnerability-based analysis.

2 Implementations of secure USB memories

2.1 Incorporated functional elements

Secure storage technology basically involves two different functions for authenticity, integrity and confidentiality of protected data: user authentication and data encryption [11], although the encryption may serve the dual purposes of authentication and data protection. Authentication is a verification process of which role is to prevent unauthorized access to the data stored in the device by applying some user input to an oracle that was priorly registered for an authenticated user [3].

Data encryption is required for confidentiality, which requires a cryptographic algorithm and a dedicated key. Only users who have the dedicated key can decrypt the secret information. Meanwhile, it is feasible to check the validity and integrity of the decrypted data once the user provides the appropriate key for the decryption. If the data does not decrypt properly, it is assumed that an incorrect key has been provided. Preserving the integrity and confidentiality of the data are the key aspects of encryption.

2.2 Implementation methodologies

Commercial secure USB flash drives can be classified into different types due to the methodologies that they use to implement the above functional elements. Some drives implement the security functions only by the host software. In this case, a normal USB memory can be used to implement a secure USB flash drive. Because the host software only has the responsibility to protect the drive, anti reverse-engineering on the software is a critical challenge.

Some other drives can support hardware-based partitions to separate hidden or secure areas from normal sections of memory in an USB memory device. Some of them have facility to encrypt data by themselves inside of the device. This approach uses a specific hardware chip such as SM3xxx from Silicon Motion to generate multiple partitions, for example. User authentication and, optionally, data encryption, are delegated to the incorporated security chip [4] and the management software only has a role of controlling the transactions.

3 Attack surface of USB flash drives

Besides the vulnerabilities to the host functions of the management software, an experienced attacker can utilize different weak points to neutralize the security mechanisms incorporated in the controller of the USB flash drive. Generally speaking, these security mechanisms are dependent of the controller's internal architecture and it may not be easy to figure out the details. However, analysis on the controllers from the inside out usually can give chances for a possible attack due to their generalized fittings that are implemented with popular core architecture and interfaces. Consequently, even a limited set of documents is sometimes enough finding considerable vulnerabilities. Figure 1 shows the block diagram of SM3524, which is the most popular USB flash controller.

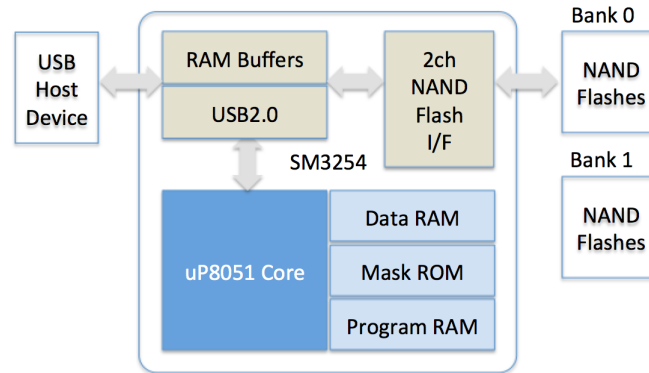


Figure 1: Block diagram of SM3254 [10]

3.1 Fishing on the bridge

Sniffing and Snooping the boundary of the controller is an essential step for research on vulnerability. Sniffing on the USB bus transactions, pin signals and memory interfaces is helpful for understanding the protocols and the internal states between the pair of the software. Stateful reconstruction between the control codes from the host driver and the commands into the NAND flash may display an outline of the implementation. Engineers need to apply some randomizing algorithms for practical implementation.

3.2 Fishing over the bridge

Dumping and Interpreting the control contexts inside the host driver and the NAND flashes is another step. Because the core architecture is based on Intel MCS51, the firmware is easily reversible when it is exposed. Because the device has Program RAM, it is assumed that an additional or upgraded code image can be downloaded into the Program RAM from the up-to-dated host driver. In this case, the program code might be exposed and reversed through scanning the host driver. Any scrambling algorithm should be applied between the host driver and the firmware of the controller to protect the context of the downloadable firmware.

Achieving critical information such as encryption keys from inside NAND flash is another problem. No flash or NVRAM is found within the block diagram, which means non-volatile information required for the security functions should be stored into the external NAND flash block. Dynamically or statically retrieving or tracing the contents of the NAND flash can provide many hints to get the critical information. Usage of pages and blocks inside the flash devices can be understandable through a time consuming analysis.

4 Analysis on USB flash drive controller

In this paper, we have prepared an environment for analyzing vulnerabilities: Microsoft Windows XP for the operating system, OllyDbg for reverse engineering, Microsoft Visual Studio for implementation, USBlyzer for checking USB communication, and DeviceTree for checking organization of device drivers.

4.1 Structure of USB flash drive controller

To confirm the manufacturer and chipset of the USB flash drive controller, we use USB sniffing and the result is shown in Figure 2.

URB	16643	11:09:48.406	6.235444 s		Bulk or Interrupt Transfer	31 bytes data	55 53 42 43 18 62 28 8A...	out
00000000 55 53 42 43 18 62 28 8A 00 08 00 00 80 00 10 F0 USB.C(.....								
00000010 02 00 00 00 00 00 00 00 04 00 00 00 00								
URB	16644-16643	11:09:48.406	6.235647 s	203 us	Bulk or Interrupt Transfer	31 bytes buffer		out
URB	16645-16642	11:09:48.406	6.235649 s	208 us	Bulk or Interrupt Transfer	31 bytes buffer		out
URB	16646	11:09:48.406	6.235652 s		Bulk or Interrupt Transfer	2048 bytes buffer		in
URB	16647	11:09:48.406	6.235654 s		Bulk or Interrupt Transfer	2048 bytes buffer		in
URB	16648-16647	11:09:48.406	6.235902 s	249 us	Bulk or Interrupt Transfer	2048 bytes data	12 01 00 02 00 00 00 40...	in
00000000 12 01 00 02 00 00 00 40 DC 09 00 10 00 11 01 02@.....								
00000010 03 01 09 02 20 00 01 01 00 80 FA 09 04 00 00 02								
00000020 08 06 50 00 07 05 81 02 00 02 FF 07 05 02 02 00P.....								
00000030 02 FF 04 03 09 04 20 03 53 00 40 00 49 00 20 00S.M.I.....								
00000040 43 00 6F 00 72 00 70 00 6F 00 72 00 61 00 74 00 C.o.r.p.o.r.a.t.....								
00000050 69 00 6F 00 6E 00 00 00 00 00 00 00 00 00 00i.o.n.....								
00000060 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00								
00000070 00 00 00 00 12 03 55 00 53 00 42 00 20 00 44 00U.S.B. .D.....								
00000080 49 00 53 00 48 00 00 00 00 00 00 00 00 00 00I.S.K.....								
00000090 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00								
000000A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00								
000000B0 00 00 22 03 41 00 41 00 30 00 30 00 30 00 30 00 "A.A.0.0.0.0.....								
000000C0 30 00 30 00 30 00 30 00 30 00 30 00 31 00 33 000.0.0.0.0.0.1.3.....								
000000D0 32 00 38 00 00 00 00 00 00 00 00 00 00 00 002.6.....								
000000E0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00								

Figure 2: Verification of the vendor for the controller by sniffing

The sniffing result shows that the manufacturer is “SMI Corporation“, but there is no chipset information. Therefore we use a special tool that is able to check SM series chipsets, and Figure 3 shows the results. This tool is called sm32xtest and it can be downloaded from the web. This result means the USB flash drive controller is SM3254AE, as shown in the IC Version field. The result is from a special tool so we cannot prove that it is correct. Therefore, we should find the actual chipset by reverse engineering.

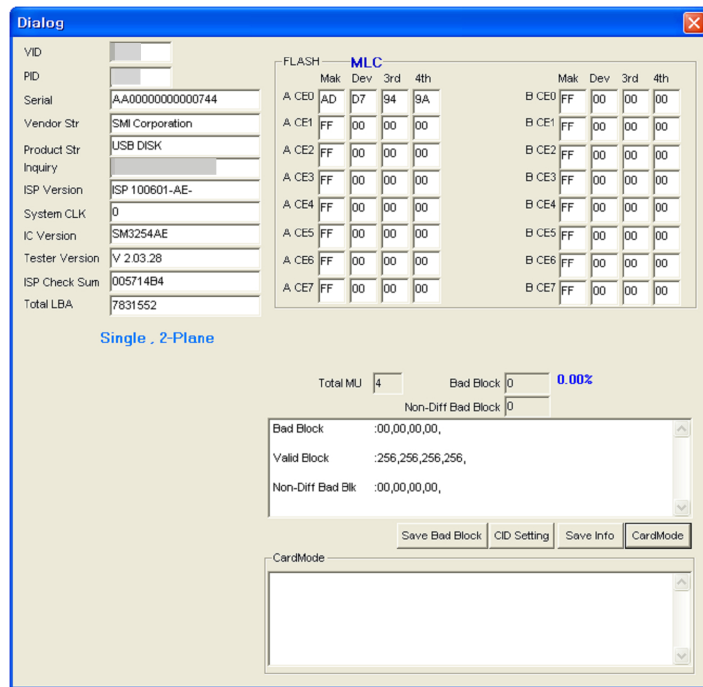


Figure 3: Verification of the vendor by a dedicated tool

First of all, we assume that the USB flash drive controller communicates with the host through spe-

cific commands, such as requesting the chipset name, connection with the secure area and so on. To verify this assumption, we analyze the communication process between application and device driver using the DeviceIoControl function. The DeviceIoControl function is called from uDiskDLL and information about the chipset is stored at offset 0x292. This result is SM3254AE and it is equal to the result we got by using the tool we already mentioned. Figure 4 shows the command requesting the chipset name and Figure 5 shows the result when receiving the chipset name.

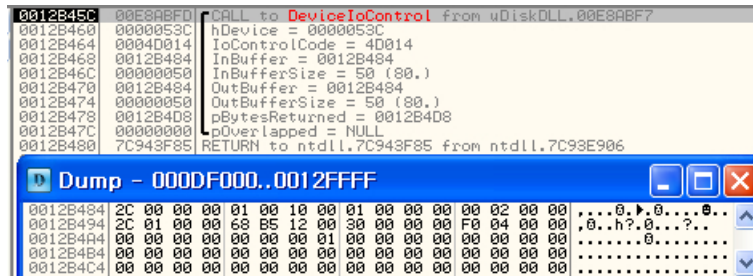


Figure 4: Reverse Engineering to find Controller Information

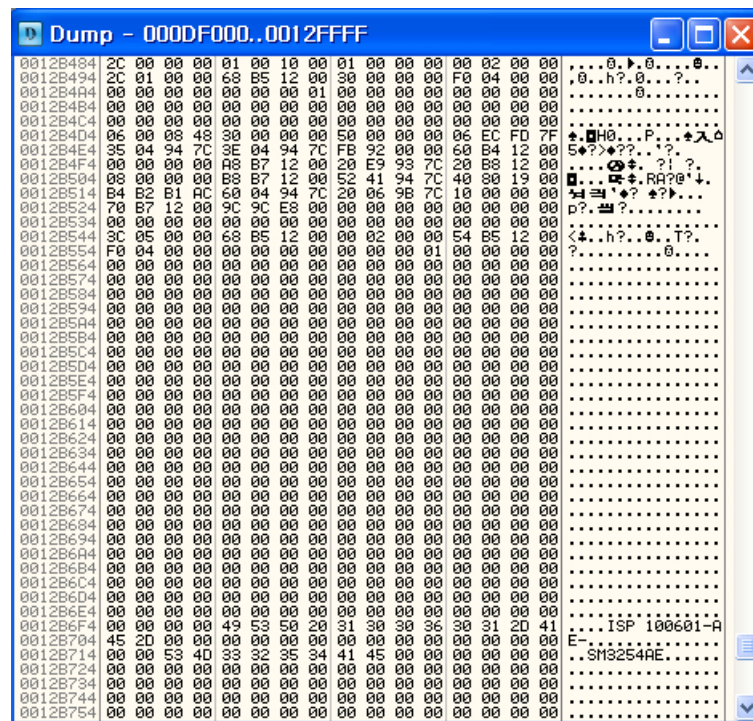


Figure 5: Controller Information in Memory

The commands used between host and USB flash drive controller are generated and transmitted by SCSICommandProcess, SCSICommandRead, and SCSICommandWrite functions inside uDiskDll, and the commands are finally sent by the DeviceIoControl function and transferred to the USB protocol. Namely, command itself comprises SCSI command format but it translates USB protocol and sends USB flash drive controller because USB flash drive controller connects to the USB interface at the host. Therefore, in order to support this communication method, the host has to insert a specific driver that it is usually called the filter driver. We check device driver structure using the DeviceTree tool and its result

is shown in Figure 6.

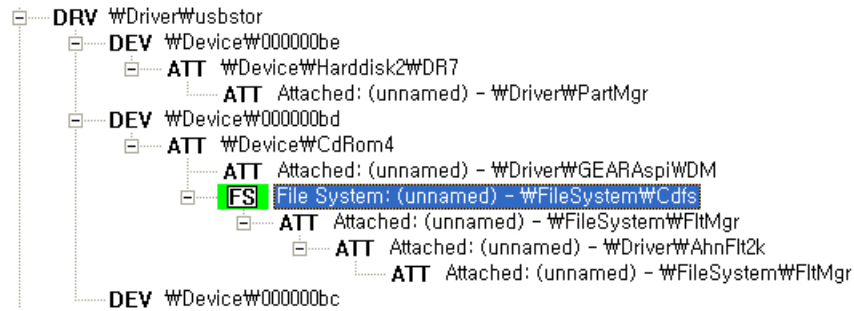


Figure 6: Device Tree for the Storage Driver

The result shows that, in order to support CDFS (CD File System), a CDFS driver is attached and we consider that GEARAspiWDM driver processes SCSI commands, and other filter drivers transform from SCSI commands to USB commands and then send those USB commands using the USB interface. Figure 7 shows the procedure for processing commands.

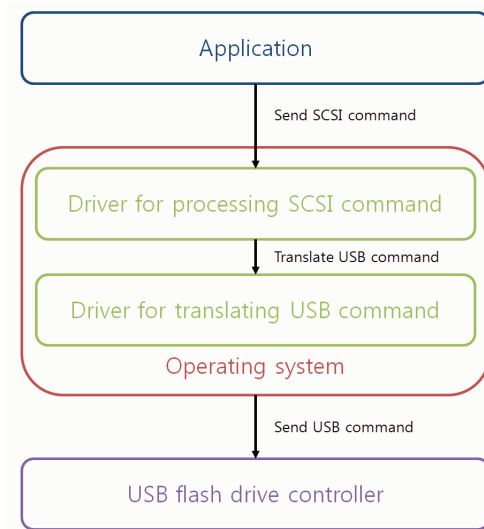


Figure 7: Command Flow for a Storage Device

4.2 Secure Area of the USB Flash Drive

Figure 7 shows the command for connecting to the secure area. The command for connecting to the secure area is 0xF111 and the address of stored information for connecting to the secure area is 0x02F7E998. If an attacker does not have information for connecting to the secure area, he or she cannot connect to it. However the information can be obtained by sending the received information for connecting to the secure area. The vulnerability in the USB flash drive controller is caused by this command. This information is necessary for user authentication and only correct users can generate this information. Due to this command, incorrect users can get this information and bypass user authentication and access stored data inside the secure area. This command is shown in Figure 8 and Figure 9 shows information received after the Send command.

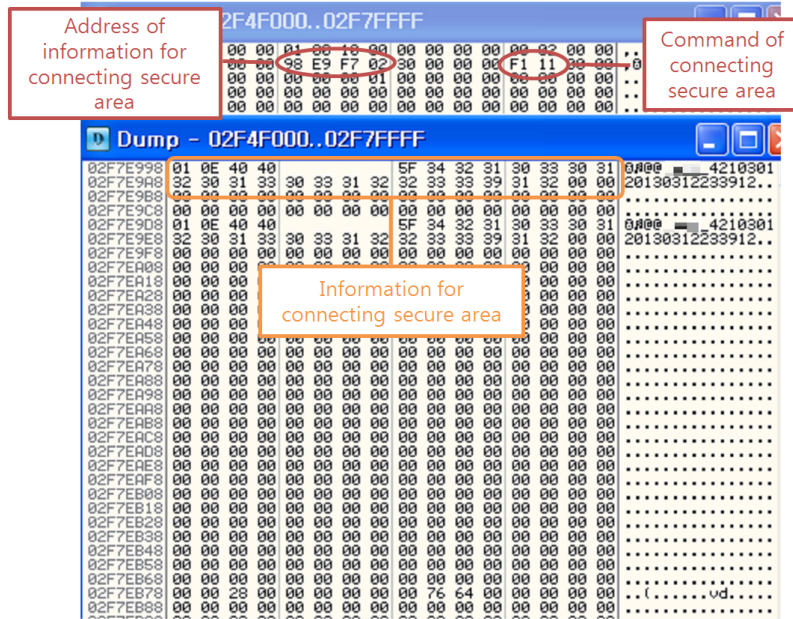


Figure 8: Command to connect the secure area

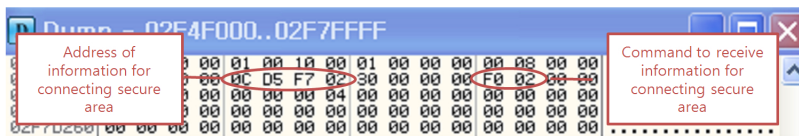


Figure 9: Subsequent command to connect the secure area

The command to receive information for connecting to the secure area is 0xF002 and information for connecting to the secure area is stored at offset 0x240 and is 32 bytes in size. If an attacker wants to connect to the secure area, the attacker sends command 0xF111 with this information. Even though this information can be different depending on the configuration of the secure USB, a third party can obtain this information by sending this command. As a result, a third party can access stored data inside the secure area without user authentication. To prove this vulnerability, we implement attack software using these commands and Figure 10 shows the result of the attack.

As shown in Figure 10, when we select the drive letter that communicates with the USB flash drive controller and then click the “Send Command“ button, we can access stored data in the “I“ drive as a secure area.

5 Conclusions

This paper analyzes a vulnerability in the SM3254AE chipset that is used to divide partitions for hardware-based partitioning method, which is one of the methods to protect data inside USB flash drive. As a result of analyzing this vulnerability, we verify that an unauthorized user can access data stored inside the secure area by analyzing for vulnerabilities. This vulnerability is caused by a specific command that responses with critical information for connecting secure area. Therefore, if manufacturers want to make stronger secure USB devices, this command must be forbidden.

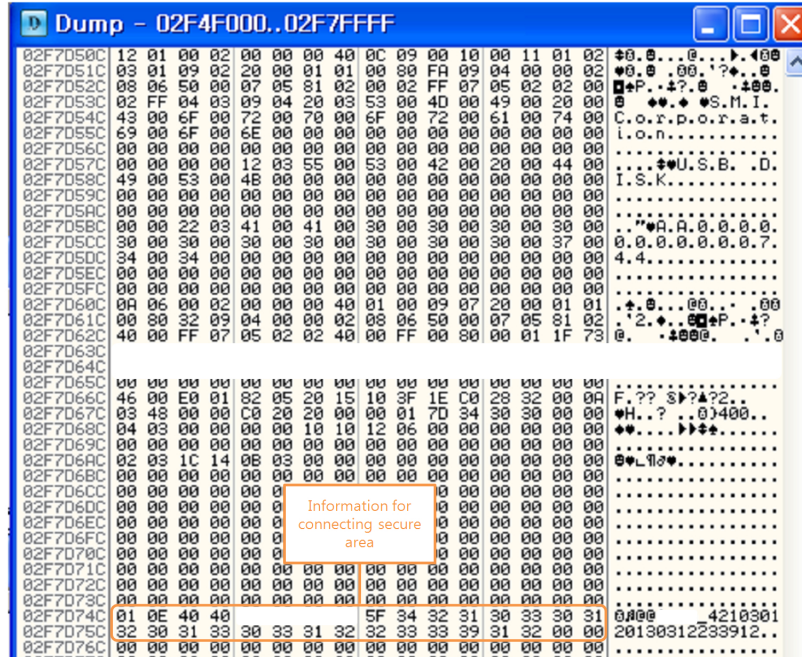


Figure 10: Parameter Block for connecting the Secure Area

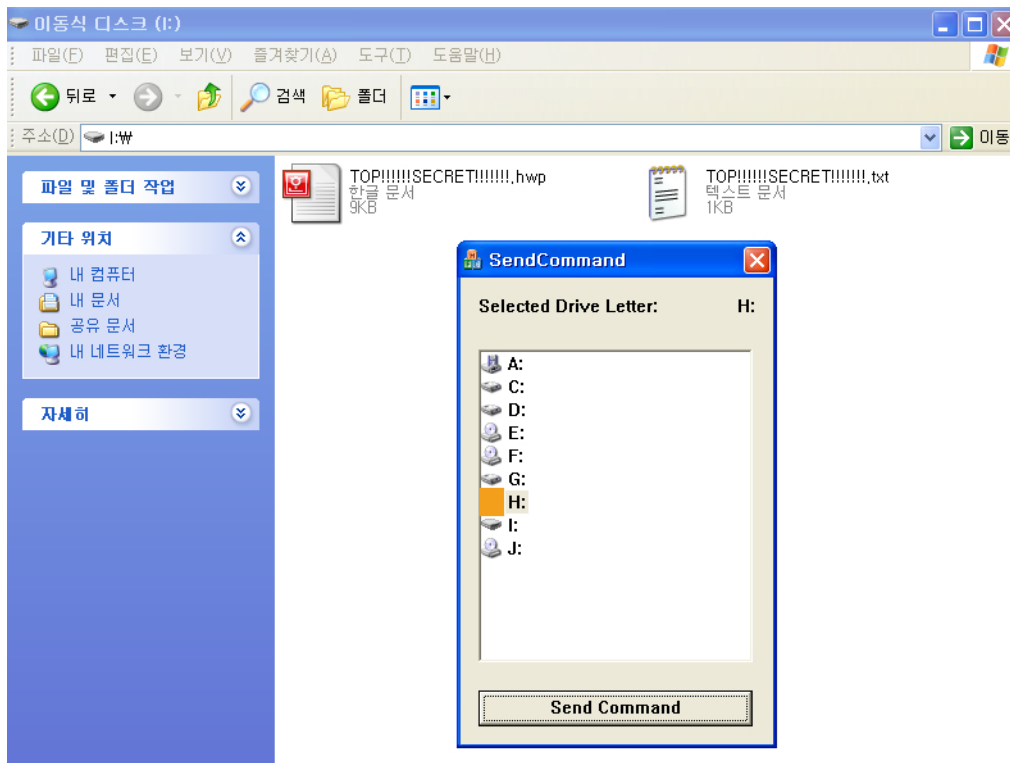


Figure 11: Implemented Software

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